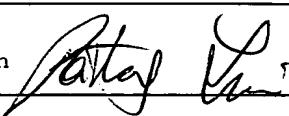


U. S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE				ATTY. DOCKET NO. <b>PD030014</b>		SERIAL NO.	
<b>INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97</b>				APPLICANTS <b>Jens Peter Wittenburg et al.</b>			
(Use several sheets if necessary)				FILING DATE Herewith		GROUP	
<b>U. S. PATENT DOCUMENTS</b>							
EXAMINEE INITIAL	DOCUMENT NUMBER	ISSUE DATE	APPLICANT/PATENTEE	CLASS	SUB-CLASS	FILING DATE IF APPROPRIATE	
AA	5,185,868	2/9/93	Tran				
AB							
AC							
AD							
AE							
AF							
AG							
AH							
<b>FOREIGN PATENT DOCUMENTS</b>							
	DOCUMENT NUMBER	PUBL. DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION Yes	No
AI	0039669	7/6/00	PCT				
AJ							
AK							
AL							
AM							
AN							
<b>OTHER INFORMATION (Including Author, Title, Pub.Date, Pertinent Pages, Country, Etc.)</b>							
AO	Yoshida et al, "A Strategy for Avoiding Pipeline Interlock Delays in a Microprocessor", Proceedings of the International Conference on Computer Design: VLSI in Computers and Processors, September 17-19 1990, pages 14-19						
AP	Search Report for EP Appln. No. 03090089 dated July 31, 2003						
AQ							
EXAMINER				DATE CONSIDERED			
SUBMITTED BY:							
Reitseng Lin				REG. NO.: 42,804		DATE: March 29, 2004	